

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	448	345/545.ccls.	US-PGPUB; USPAT	OR	OFF	2005/06/30 10:15
S1	685	345/501.ccls.	US-PGPUB; USPAT	OR	OFF	2005/06/30 08:25
S2	159	345/547.ccls.	US-PGPUB; USPAT	OR	OFF	2005/06/29 10:09
S3	124	345/547.ccls. and pixel\$1	US-PGPUB; USPAT	OR	OFF	2005/06/27 09:02
S4	6	345/547.ccls. and pixel\$1 and dummy	US-PGPUB; USPAT	OR	OFF	2005/06/30 09:44
S5	12	("4807189" "5097257" "5274753" "5345554" "5412399" "5517612" "5625379").PN. OR ("5896140").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/27 10:29
S6	382	dummy adj pixel\$1	US-PGPUB; USPAT	OR	OFF	2005/06/29 10:45
S7	27	dummy adj pixel\$1 and frame adj buffer	US-PGPUB; USPAT	OR	OFF	2005/06/27 10:34
S8	22	dummy adj pixel\$1 and frame adj buffer and parallel	US-PGPUB; USPAT	OR	OFF	2005/06/27 10:34
S9	22	dummy adj pixel\$1 and frame adj buffer and parallel	US-PGPUB; USPAT	OR	OFF	2005/06/27 10:34
S10	4	dummy adj pixel\$1 and frame adj buffer and parallel and serial	US-PGPUB; USPAT	OR	OFF	2005/06/28 14:41
S11	60	dummy adj pixel\$1 and frame and parallel and serial	US-PGPUB; USPAT	OR	OFF	2005/06/27 10:36
S12	39	dummy adj pixel\$1 and frame and parallel same serial	US-PGPUB; USPAT	OR	OFF	2005/06/27 10:43
S13	3	dummy adj pixel\$1 and frame adj memory and parallel same serial	US-PGPUB; USPAT	OR	OFF	2005/06/28 09:31
S14	0	dummy adj pixel\$1 and frame and parallel same serial and time adj series	US-PGPUB; USPAT	OR	OFF	2005/06/27 10:43
S15	3	dummy same pixel\$1 and frame and parallel same serial and time adj series	US-PGPUB; USPAT	OR	OFF	2005/06/27 10:45
S16	10	dummy same pixel\$1 and frame and parallel and series and time adj series	US-PGPUB; USPAT	OR	OFF	2005/06/27 10:45

S17	25	("4710761" "5146211" "5146592" "5179639" "5208583" "5208745" "5216413" "5233686" "5264837" "5276437" "5293540" "5319382" "5345554" "5375203" "5398075" "5404437" "5432905" "5608864").PN. OR ("5754170").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/27 16:38
S18	0	parallel adj to adj serial	US-PGPUB; USPAT	OR	OFF	2005/06/28 09:32
S19	1695	parallel-serial	US-PGPUB; USPAT	OR	OFF	2005/06/28 09:32
S20	829	parallel-serial adj convert\$3	US-PGPUB; USPAT	OR	OFF	2005/06/28 09:32
S21	132	parallel-serial adj convert\$3 and pixel\$1	US-PGPUB; USPAT	OR	OFF	2005/06/28 09:33
S22	10	parallel-serial adj convert\$3 and pixel\$1 and dummy	US-PGPUB; USPAT	OR	OFF	2005/06/28 14:01
S23	7	parallel-serial adj convert\$3 and pixel\$1 and dummy and register\$1	US-PGPUB; USPAT	OR	OFF	2005/06/28 14:28
S24	4	parallel-serial adj convert\$3 and pixel\$1 and dummy and register\$1 and shift	US-PGPUB; USPAT	OR	OFF	2005/06/28 15:28
S25	1	parallel-serial adj convert\$3 and pixel\$1 and dummy and register\$1 and shift and split	US-PGPUB; USPAT	OR	OFF	2005/06/28 14:38
S26	62	split adj shift adj register\$1	US-PGPUB; USPAT	OR	OFF	2005/06/28 14:44
S27	52	split adj shift adj register\$1 and latch	US-PGPUB; USPAT	OR	OFF	2005/06/28 14:39
S28	51	split adj shift adj register\$1 and latch and clock	US-PGPUB; USPAT	OR	OFF	2005/06/28 14:44
S29	46	split adj shift adj register\$1 and latch and clock and image	US-PGPUB; USPAT	OR	OFF	2005/06/28 14:40
S30	46	split adj shift adj register\$1 and latch and clock and image and pixel\$1	US-PGPUB; USPAT	OR	OFF	2005/06/28 14:40
S31	46	split adj shift adj register\$1 and latch and clock and image and pixel\$1 and video	US-PGPUB; USPAT	OR	OFF	2005/06/28 14:45
S32	15	split adj shift adj register\$1 and latch and clock and image and pixel\$1 and video and dummy	US-PGPUB; USPAT	OR	OFF	2005/06/28 14:41

S34	15	split adj shift adj register\$1 and latch and clock and image and pixel\$1 and video and dummy and serial	US-PGPUB; USPAT	OR	OFF	2005/06/28 14:41
S35	15	split adj shift adj register\$1 and latch and clock and image and pixel\$1 and video and dummy and serial and parallel	US-PGPUB; USPAT	OR	OFF	2005/06/28 14:41
S36	8	split adj shift adj register\$1 and latch and clock and image and pixel\$1 and video and dummy and serial same parallel	US-PGPUB; USPAT	OR	OFF	2005/06/28 14:41
S37	1	dummy adj pixel\$1 and frame adj buffer and parallel adj serial	US-PGPUB; USPAT	OR	OFF	2005/06/28 14:41
S38	0	split adj shift adj register\$1 and latch and clock and image and dummy adj pixel\$1 and video	US-PGPUB; USPAT	OR	OFF	2005/06/28 14:44
S39	0	split adj shift adj register\$1 and dummy adj pixel\$1	US-PGPUB; USPAT	OR	OFF	2005/06/28 14:44
S40	8	split adj shift adj register\$1 and latch and clock and image and pixel\$1 and video and serial adj parallel	US-PGPUB; USPAT	OR	OFF	2005/06/28 14:45
S41	71	parallel-serial adj convert\$3 and pixel\$1 and register\$1 and shift	US-PGPUB; USPAT	OR	OFF	2005/06/28 15:28
S42	21	parallel-serial adj convert\$3 and pixel\$1 and register\$1 and shift and serial-parallel adj convert\$3	US-PGPUB; USPAT	OR	OFF	2005/06/28 15:28
S43	14	parallel-serial adj convert\$3 and pixel\$1 and shift adj register\$1 and serial-parallel adj convert\$3	US-PGPUB; USPAT	OR	OFF	2005/06/28 15:29
S44	6	parallel-serial adj convert\$3 same shift adj register\$1 and pixel\$1 and serial-parallel adj convert\$3	US-PGPUB; USPAT	OR	OFF	2005/06/28 15:29
S45	6	parallel-serial adj convert\$3 same shift adj register\$1 and pixel\$1 and serial-parallel adj convert\$3 and clock	US-PGPUB; USPAT	OR	OFF	2005/06/28 15:30
S46	1	parallel-serial adj convert\$3 same shift adj register\$1 and pixel\$1 and serial-parallel adj convert\$3 and clock and selector	US-PGPUB; USPAT	OR	OFF	2005/06/28 15:29
S47	14	parallel-serial adj convert\$3 and shift adj register\$1 and pixel\$1 and serial-parallel adj convert\$3 and clock	US-PGPUB; USPAT	OR	OFF	2005/06/28 15:39
S48	14	parallel-serial adj convert\$3 and shift adj register\$1 and pixel\$1 and serial-parallel adj convert\$3	US-PGPUB; USPAT	OR	OFF	2005/06/28 15:41

S50	148	parallel-serial adj convert\$3 and shift adj register\$1 and serial-parallel adj convert\$3	US-PGPUB; USPAT	OR	OFF	2005/06/28 15:41
S51	40	parallel-serial adj convert\$3 and shift adj register\$1 and serial-parallel adj convert\$3 and image\$1	US-PGPUB; USPAT	OR	OFF	2005/06/28 15:41
S52	25	parallel-serial adj convert\$3 and shift adj register\$1 and serial-parallel adj convert\$3 and image\$1 and video	US-PGPUB; USPAT	OR	OFF	2005/06/28 15:41
S53	4	parallel-serial adj convert\$3 and shift adj register\$1 and serial-parallel adj convert\$3 and image\$1 and video and dummy	US-PGPUB; USPAT	OR	OFF	2005/06/28 15:46
S54	10	parallel-serial adj convert\$3 and shift adj register\$1 and serial-parallel adj convert\$3 and image\$1 and dummy	US-PGPUB; USPAT	OR	OFF	2005/06/28 15:48
S55	4	parallel-serial adj convert\$3 same shift adj register\$1 and serial-parallel adj convert\$3 and image\$1 and dummy	US-PGPUB; USPAT	OR	OFF	2005/06/28 15:48
S56	16	parallel-serial adj convert\$3 same shift adj register\$1 and serial-parallel adj convert\$3 and image\$1	US-PGPUB; USPAT	OR	OFF	2005/06/28 15:48
S57	13	parallel-serial adj convert\$3 same shift adj register\$1 and serial-parallel adj convert\$3 and image\$1 and video	US-PGPUB; USPAT	OR	OFF	2005/06/28 15:48
S58	17	("4196450" "4736251" "4774566" "4777536" "4809061" "4866535" "4891690" "4894730" "5032928" "5038207").PN. OR ("5305122").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/28 16:03
S59	3	("5305122" "6198546" "6222649").PN. OR ("6621598").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/28 16:23
S61	35	scan adj line same padding	US-PGPUB; USPAT	OR	OFF	2005/06/29 14:35
S62	22	scan adj line same padding and frame	US-PGPUB; USPAT	OR	OFF	2005/06/29 10:09
S63	22	scan adj line same padding and frame and pixel\$1	US-PGPUB; USPAT	OR	OFF	2005/06/29 10:19
S64	6	scan adj line same padding and frame adj buffer and pixel\$1	US-PGPUB; USPAT	OR	OFF	2005/06/29 10:26

S65	42	scan adj line and padding and frame adj buffer and pixel\$1	US-PGPUB; USPAT	OR	OFF	2005/06/29 10:26
S66	38	scan adj line and padding and frame adj buffer and pixel\$1 and digital	US-PGPUB; USPAT	OR	OFF	2005/06/29 10:26
S67	34	scan adj line and padding and frame adj buffer and pixel\$1 and digital and video	US-PGPUB; USPAT	OR	OFF	2005/06/29 10:26
S68	33	scan adj line and padding and frame adj buffer and pixel\$1 and digital and video and image\$1	US-PGPUB; USPAT	OR	OFF	2005/06/29 10:27
S69	20	("5063437" "5101277" "5479604" "5585864" "5663765" "5675390" "5936676" "5978545" "6002442" "6005630" "6011583" "6040869" "6046721" "6055018" "6069607" "6072530" "6088045" "6326935").PN. OR ("6567097").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/29 10:40
S70	4	insert\$3 adj dummy adj pixel\$1	US-PGPUB; USPAT	OR	OFF	2005/06/29 11:05
S71	0	append\$3 adj dummy adj pixel\$1	US-PGPUB; USPAT	OR	OFF	2005/06/29 11:05
S72	0	embed\$4 adj dummy adj pixel\$1	US-PGPUB; USPAT	OR	OFF	2005/06/29 11:05
S73	9	insert\$3 same dummy adj pixel\$1	US-PGPUB; USPAT	OR	OFF	2005/06/29 11:01
S75	1	append\$3 same dummy adj pixel\$1	US-PGPUB; USPAT	OR	OFF	2005/06/29 11:05
S76	3	embed\$4 same dummy adj pixel\$1	US-PGPUB; USPAT	OR	OFF	2005/06/29 11:05
S77	1764	insert\$3 adj pad\$4	US-PGPUB; USPAT	OR	OFF	2005/06/29 11:06
S79	61	insert\$3 adj pad\$4 and pixel\$1	US-PGPUB; USPAT	OR	OFF	2005/06/29 11:06
S80	38	insert\$3 adj pad\$4 and pixel\$1 and frame	US-PGPUB; USPAT	OR	OFF	2005/06/29 11:06
S81	8	insert\$3 adj pad\$4 and pixel\$1 and frame adj buffer	US-PGPUB; USPAT	OR	OFF	2005/06/29 14:33
S82	8	insert\$3 adj pad\$4 and pixel\$1 and frame adj buffer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/29 14:34

S83	8	(insert\$3 append\$3 embed\$4) adj pad\$4 and pixel\$1 and frame adj buffer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/29 14:36
S84	38	(raster scan) adj line same padding	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/29 14:35
S85	334	(raster scan) adj line same (padding null redundant)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/29 14:35
S86	22	(insert\$3 append\$3 embed\$4) adj (pad\$4 null redundant) and pixel\$1 and frame adj buffer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/29 14:57
S87	33	wxga	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/29 14:37
S88	28	wvga	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/29 14:37
S89	12	wxga and dummy	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/29 14:39
S90	13	wxga and (dummy null redundant padding)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/29 14:45

S91	7	wvga and (dummy null redundant padding)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/29 14:40
S92	4	wxga and (dummy null redundant padding) and wvga	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/29 14:45
S93	119	(insert\$3 append\$3 embed\$4) adj (pad\$4 null redundant) and pixel\$1 and frame	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/29 15:01
S94	16	("5307450" "5420966" "5513366" "5758120" "5764243" "5764787" "5767856" "5767858" "5821950" "5828378" "5864342" "5886701" "5940086" "5966528" "6002407").PN. OR ("6417848"). URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/06/29 14:58
S95	0	(insert\$3 append\$3 embed\$4) adj (pad\$4 null redundant) same scan adj line and pixel\$1 and frame	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/29 15:01
S96	0	(insert\$3 append\$3 embed\$4) adj (pad\$4 null redundant) same raster adj line and pixel\$1 and frame	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/29 15:02
S97	31	(insert\$3 append\$3 embed\$4) adj (pad\$4 null redundant dummy) same line and pixel\$1 and frame	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/29 15:02
S98	3	(insert\$3 append\$3 embed\$4) adj (pad\$4 null redundant dummy) same scan adj line and pixel\$1 and frame	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/29 15:02

Day : Thursday
Date: 6/30/2005


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Time: 10:14:10

Inventor Name Search Result

Your Search was:

Last Name = INOUE

First Name = TOSHIAKI

Application#	Patent#	Status	Date Filed	Title	Inventor Name 22
<u>10810618</u>	Not Issued	030	03/29/2004	DIGITAL IMAGE PROCESSING DEVICE	INOUE, TOSHIAKI
<u>10713005</u>	Not Issued	030	11/17/2003	METHOD AND DEVICE FOR ACCESSING FRAME MEMORY WITHIN DISPLAY PANEL DRIVER	INOUE, TOSHIAKI
<u>09973717</u>	<u>6566185</u>	150	10/11/2001	METHOD OF MANUFACTURING A PLURAL UNIT HIGH FREQUENCY TRANSISTOR	INOUE, TOSHIAKI
<u>09958824</u>	Not Issued	161	02/01/2002	PACKING CONTAINER AND PACKING METHOD	INOUE, TOSHIAKI
<u>09597158</u>	<u>6332966</u>	150	06/20/2000	AIR/FUEL RATIO DETECTING ARRANGEMENT	INOUE, TOSHIAKI
<u>09546261</u>	<u>6465850</u>	150	04/10/2000	SEMICONDUCTOR DEVICE	INOUE, TOSHIAKI
<u>09385357</u>	<u>6704762</u>	150	08/30/1999	MULTIPLIER AND ARITHMETIC UNIT FOR CALCULATING SUM OF PRODUCT	INOUE, TOSHIAKI
<u>09249579</u>	<u>6346728</u>	150	02/12/1999	PLURAL TRANSISTOR DEVICE WITH MULTI-FINGER STRUCTURE	INOUE, TOSHIAKI
<u>09119623</u>	<u>6243083</u>	150	07/21/1998	VIDEO SIGNAL PROGRESSING CIRCUIT	INOUE, TOSHIAKI
<u>09113291</u>	<u>6219777</u>	150	07/10/1998	REGISTER FILE HAVING SHARED AND LOCAL DATA WORD PARTS	INOUE, TOSHIAKI
<u>09025998</u>	<u>5852318</u>	150	02/19/1998	SEMICONDUCTOR DEVICE	INOUE, TOSHIAKI
<u>09025825</u>	Not Issued	161	02/19/1998	SEMICONDUCTOR DEVICE	INOUE, TOSHIAKI

<u>08900067</u>	<u>5831390</u>	150	07/23/1997	CRT WITH A TRANSPARENT FILM AND A COMPENSATING ELECTRODE	INOUE, TOSHIAKI
<u>08797121</u>	<u>5933362</u>	150	02/10/1997	METHOD OF ADDING TWO BINARY NUMBERS AND BINARY ADDER USED THEREIN	INOUE, TOSHIAKI
<u>08127008</u>	Not Issued	161	09/27/1993	VECTOR OPERATION PROCESSOR INCLUDING RESOURCE STATUS TABLE AND ADDRESS GENERATION COMPLETION FLAG	INOUE, TOSHIAKI
<u>07445058</u>	<u>5102922</u>	150	12/04/1989	FOAMABLE COMPOSITION AND PROCESS FOR PREPARING THE SAME AND RIGID FOAMED ARTICLE AND PROCESS FOR PREPARING THE SAME	INOUE, TOSHIAKI
<u>07096226</u>	<u>4743909</u>	150	09/04/1987	METHOD AND APPARATUS FOR SETTING DIRECTION OF A PARABOLIC ANTENNA RELATIVE TO A COMMUNICATING SATELLITE	INOUE, TOSHIAKI
<u>06906848</u>	<u>RE32985</u>	150	09/15/1986	CREDIT TRANSACTION PROCESSING SYSTEM	INOUE, TOSHIAKI
<u>06715452</u>	<u>4674620</u>	250	03/25/1985	METHOD OF POSITIONING PALLETS AND DEVICE THEREOF	INOUE, TOSHIAKI
<u>06715451</u>	<u>4671402</u>	150	03/25/1985	FREE FLOW CONVEYOR	INOUE, TOSHIAKI
<u>06634994</u>	Not Issued	166	07/27/1984	METHOD OF AND APPARATUS FOR SETTING DIRECTION OF A PARABOLIC ANTENNA RELATIVE TO A COMMUNICATING SATELLITE	INOUE, TOSHIAKI
<u>06512022</u>	<u>4594663</u>	150	07/08/1983	CREDIT TRANSACTION PROCESSING SYSTEM	INOUE, TOSHIAKI

Inventor Search Completed: No Records to Display.

Search Another: Inventor

Last Name	First Name
Inoue	Toshiaki

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